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Compact four-way suspended-stripline power divider with low loss and high isolation

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Research Paper

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Abstract

A four-way suspended-stripline power divider is presented in this letter. The power dividing network is designed by using the suspended stripline, while the isolation network is designed by using the microstrip line. The vias are used to connect the power dividing network and the isolation network. The even- and odd-mode analysis method is applied to design the presented power divider. The simulated and measured results of the presented power divider show reasonable agreement with each other. The measured input return loss in the band is greater than 28 dB (7.92 to 9.53 GHz), while the measured insertion loss is less than 0.37 dB. The measured output return loss is greater than 20 dB from 7.82 to 9.86 GHz. Besides, the measured output isolation is greater than 20 dB.

Introduction

Power dividers have been widely used in radar systems such as power combining amplifiers and antenna arrays [1–12], and higher demands are placed on compact, low loss, and high isolation. In recent years, many kinds of multi-way power dividers, based on the waveguide, microstrip line or SIW, have been widely investigated. In [6, 7, 12], waveguide power dividers are investigated with low insertion loss. However, the isolation between output ports is not high and it is difficult to achieve miniaturization and integration within one wavelength. Meanwhile, in [4, 9], the power dividers designed with a microstrip line and slotline show the potential of compact size and easy integration with active devices. In [5, 8, 10], power dividers based on the substrate integrated waveguide (SIW) are designed. Good isolation between the four output ports and good impedance matching at all the ports is achieved. However, the insertion losses caused by microstrip-to-SIW, microstrip-to-half mode substrate integrated waveguide (HMSIW) transitions, and the SMA connectors are large that cannot meet the demands of high power-combining efficiency.

The suspended stripline is widely used in filter design due to its low insertion loss [13, 14]. In [10], suspended striplines are applied in the design of the power divider to reduce the insertion loss. However, it is cannot meet the demands for high isolation.

In this letter, a four-way suspended-stripline power divider at the X band with compact size and good input/output return loss is designed and fabricated. The suspended stripline is introduced to the designed power-divider network and connected to the SMA connectors with low insertion loss. The microstrip lines are introduced to obtain attractive high isolation without effect input return loss and insertion loss.

Structure, analysis, and design

Figures 1 and 2 demonstrate the structure of the suspended-stripline power divider. The presented power divider includes the power-dividing circuit and the isolation network. The suspended stripline has been used in the power dividing circuit, while the microstrip line has been used in the isolation network. The power dividing network and isolation network are connected through vias, as shown in Fig. 1. Four isolation resistors are located on the microstrip isolation circuit with a common point, as shown in Fig. 2(b).

Since the power divider is almost symmetric, the even- and odd-mode equivalent circuit method can be applied to analyze the presented four-way suspended-stripline power divider. Fig. 3 shows the equivalent circuit model of the proposed four-way power divider. Under the even-mode excitation, the equivalent circuit can be simplified to Fig. 3(b). According to transmission line theory, the impedance Z_{ine1} and Z_{ine2} can be given by

$$Z_{ine1} = -jZ_{b1}\cot\theta_1 \quad (1)$$

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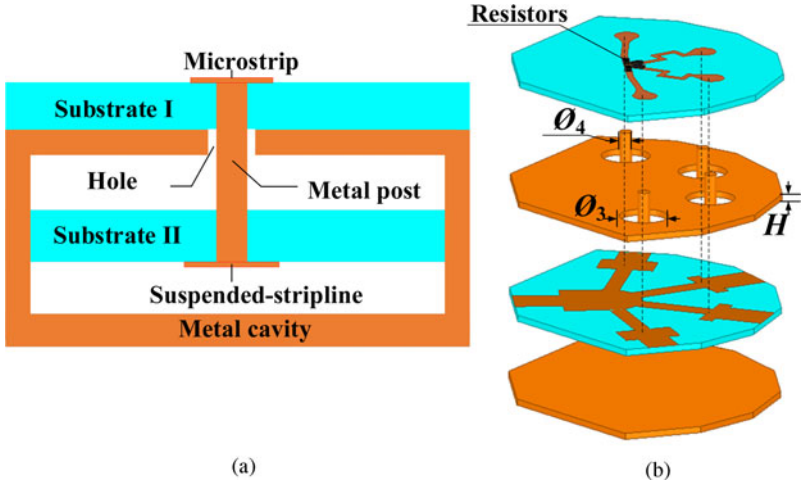


Fig. 1. Quasi-planar suspended-stripline power divider: (a) section structure view and (b) 3D structure view.

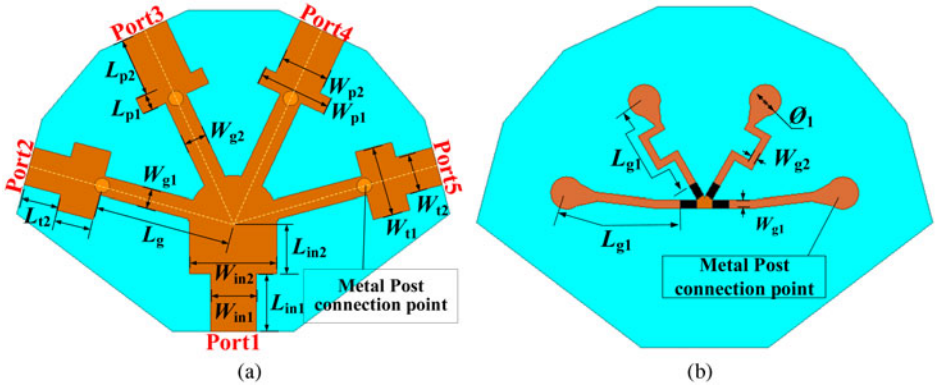


Fig. 2. (a) Suspended-stripline power divider network and (b) microstrip isolation network.

$$Z_{ine2} = Z_{b2}\frac{Z_{ine1} + jZ_{b2}\tan\theta_2}{Z_{b2} + jZ_{ine1}\tan\theta_2} = Z_{b2}\frac{-jZ_{b1} + jZ_{b2}\tan\theta_1\tan\theta_1}{Z_{b2}\tan\theta_1 + Z_{b1}\tan\theta_2} \quad (2)$$

where Z_{b1} is the impendence of the microstrip line and Z_{b2} is the impendence of the transmission lines through the vias. In the even mode, Z_{ine2} should be equal to infinity, also $Z_a = 2Z_0$ to satisfy the matching condition. So admittance $Y_{ine2} = 1/Z_{ine2} = 0$. When $\theta_1 + \theta_2 = \pi$ is satisfied, the impedance ratio can be obtained as

$$\frac{Z_{b1}}{Z_{b2}} = \frac{-\tan\theta_2}{\tan\theta_1} = 1 \quad (3)$$

Under the odd-mode excitation, the equivalent circuit of the presented power divider can be simplified to Fig. 3(c). According to transmission line theory, the impedance Z_{ino1} and Z_{ino2} can be given by

$$Z_{ino1} = Z_{b1}\frac{R + jZ_{b1}\tan\theta_1}{Z_{b1} + jR\tan\theta_1} \quad (4)$$

$$Z_{ino2} = Z_{b2}\frac{Z_{ino1} + jZ_{b2}\tan\theta_2}{Z_{b2} + jZ_{ino1}\tan\theta_2} \quad (5)$$

When $\theta_1 + \theta_2 = \pi$ and $Z_{ino2} = Z_0$ are satisfied, it becomes

$$\frac{R}{Z_0} = \frac{(Z_{b1}Z_{b2} + Z_{b1}^2\tan^2\theta_1)}{(Z_{b1}Z_{b2} + Z_{b2}^2\tan^2\theta_1)} \quad (6)$$

$$RZ_0 = Z_{b1}Z_{b2} \quad (7)$$

According to equations (3), (6), and (7), the isolation network can be easily designed.

When the center frequency is determined, the suspended-stripline wavelength λ_{g1} and the microstrip wavelength λ_{g2} can be obtained. The electrical length of the high impendence Z_a suspended stripline is $\pi/2$. The length L_g can be further designed. When the length and impendence of the transmission lines through the vias is determined, according to the above formula, the length L_{g1} and width of the isolation microstrip network is easy to be obtained. To obtain good isolation, the transmission lines through the vias are connected to connect the place $\lambda_{g1}/4$ of the high impedance suspended-striplines. And the two-step impedance lines are used to obtain good impedance matching of input and output ports respectively.

Figure 4 shows the input return loss S_{11} and output return loss S_{33} with different $Ø_3$. It can be seen that the input and output return loss can adjust greatly by altering $Ø_3$. When the diameter $Ø_4$ of the inner conductor is initially identified, by properly adjusting $Ø_3$, good impedance matching is obtained. When $Ø_3$

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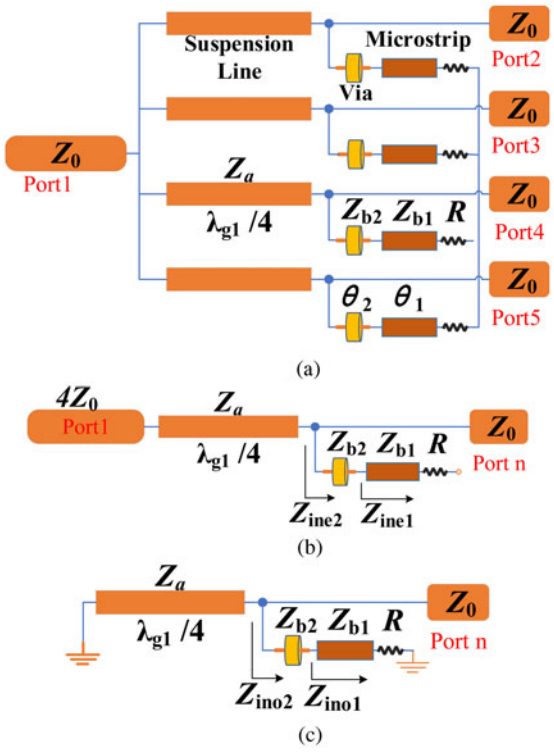


Fig. 3. (a) Equivalent circuit of the proposed power divider, (b) even mode, and (c) odd mode.

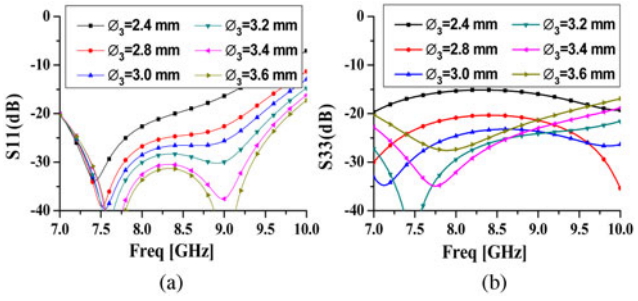


Fig. 4. (a) Input return loss and (b) output return loss with varied $Ø_3$.

is varied, the input and output return loss is changed greatly, as shown in Fig. 4.

Results and discussion

A four-way suspended-stripline power divider was designed according to the aforementioned analysis. It was simulated and optimized in the full-wave simulation software HFSS. The four-way power divider has been fabricated by machining of the suspended-stripline in the copper blocks and the microstrip line on the substrate Taconic RF-35 with a relative dielectric constant of 3.5, a thickness of 0.508 mm, and a loss tangent of 0.0018. The final dimensions of the proposed power divider is as follows: $W_{t2} = 2.49$ mm, $W_{t1} = 4.67$ mm, $L_{t2} = 2.45$ mm, $L_g = 9$ mm, $W_{p1} = 3.42$ mm, $W_{p2} = 4.91$ mm, $L_{p2} = 3.87$ mm, $L_{p1} = 1.13$ mm, $W_{in1} = 1.00$ mm, $L_{in1} = 3.88$ mm, $L_{in2} = 3.5$ mm, $W_{in2} = 1.88$ mm, $W_{g1} = 0.52$ mm, $W_{g2} = 0.45$ mm, $Ø_3 = 3.14$ mm, $Ø_4 = 1.00$ mm, $Ø_1 = 2$ mm, $L_{g1} = 7.72$ mm. The resistance is 50 Ω and is selected

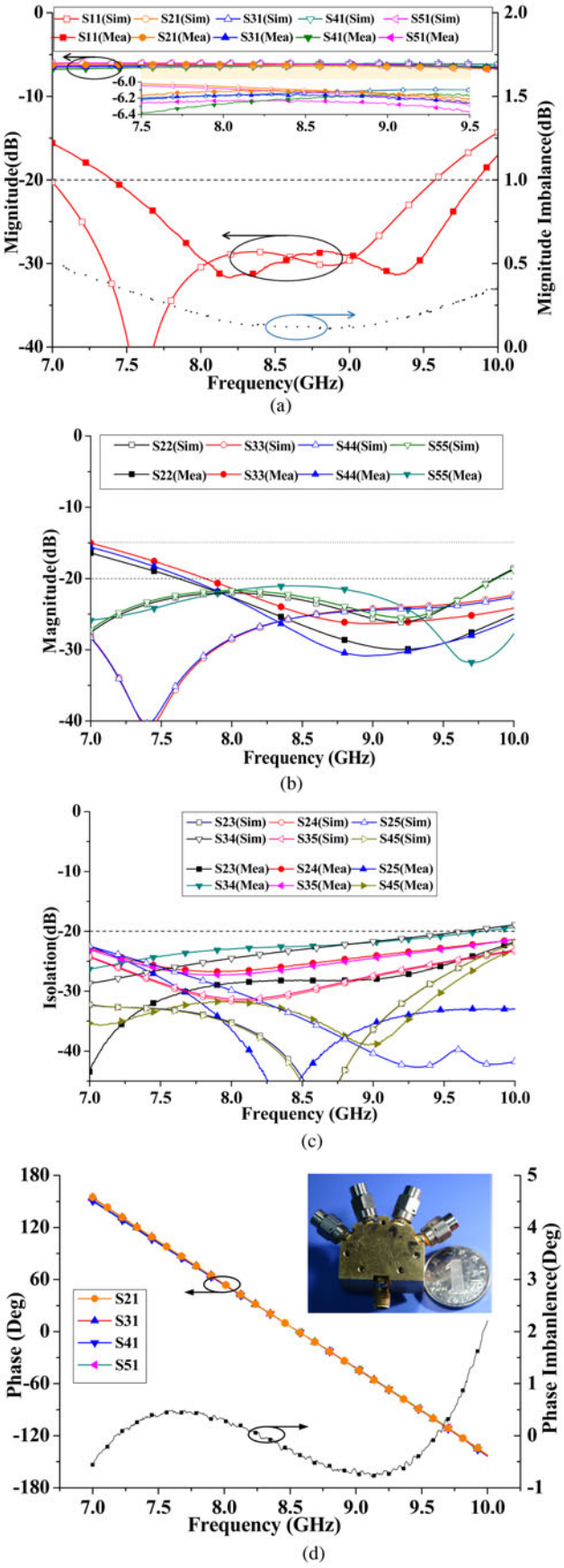


Fig. 5. Simulated and measured results of the proposed power divider: (a) input return loss and insertion loss, (b) output return loss, (c) isolation, and (d) phase.

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Table 1. Comparisons between the proposed power divider and the reported ones

Works	Frequency (GHz)	IRL (dB)	ORL (dB)	Size (λ_{g2})	Iso (dB)	Ins (dB)	Str
This work	7.92–953	>28	>20	0.8 × 0.6	>20	<0.37	SSL
[4]	1–1.1	>20	20	0.48 × 0.48	>20	>1	ML
[5]	17–19	12.5	10	3.29 × 0.8	20	2.25	SIW
[7]	3–10.6	>15	/	/	10	0.4	RC
[10]	9.83–10.8	>10	10	2.81 × 0.89	>10	1.5	SIW
[12]	10	14	/	/	>8	1	RCW

from Barry's high-power resistors. The photograph of the fabricated suspended-stripline power divider is shown in Fig. 5(d).

The fabricated power divider has been measured by using an Agilent N5244A network analyzer. The simulated and measured results are presented in Fig. 5. It can be seen that the measured results agree with the simulated ones over the entire operating frequency range. The measured 20-dB input return loss bandwidth is 2.45 GHz (from 7.40 to 9.85 GHz). Moreover, the measured input return loss is greater than 28 dB from 7.92 to 9.53 GHz. The measured insertion loss is less than 0.37 dB from 7.5 to 9.5 GHz while the measured magnitude imbalance is less than 0.5 dB. As shown in Fig. 5(b), the output return loss is greater than 20 dB from 7.82 to 9.86 GHz. The output isolation is greater than 20 dB from 7 to 9.77 GHz, as shown in Fig. 5(c). Meanwhile, the measured phase imbalance between the output ports is less than ± 0.7°. The measured results and the simulated ones show a reasonable agreement with each other. As can be seen, the differences between the simulated and measured results are due to the welding and manufacturing error of the power divider. The comparison with others' four-way power dividers is shown in Table 1. It can be seen that the presented suspended-stripline power divider has the advantages of low insertion loss, relatively high isolation, and good input/output return loss.

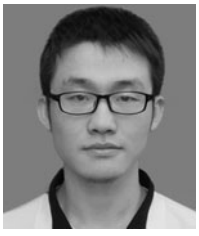
Conclusion

In this letter, a four-way suspended-stripline power divider is demonstrated. The circuit model and the performance of the simulated and measured results have been introduced and discussed. The measured results are in good agreement with the simulated ones. The proposed four-way suspended-stripline power divider has low insertion loss, good input/output return loss, compact size, and high isolation. The proposed power divider can be used to multiway power-combining amplifiers, antenna array, mixer, and so on.

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